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ABSTRACT OF THE INVENTION

A microfabricated capillary array electrophoresis chip includes a planar substrate having a first major surface defining converging first and second elongate separation channels. Each separation channel extends between an associated cathode port and anode port defined by the first major surface. The substrate further comprises a first perimetrical edge segment extending substantially along the first separation channel, and a second perimetrical edge segment extending substantially along the second separation channel. The perimetrical edge segments of a pair of chips of the present invention are cooperatively engageable so as to provide an electrophoresis separation plate having psuedoradially-aligned separation channels.